

AMENDMENT TO THE SPECIFICATION

Please amend paragraph [0028] as follows:

[0028] The LINC modulator 100 and channel equalization filters may be implemented digitally, using high speed analog to digital converters (ADC) and digital to analog converters (DAC) to provide the interface between the continuous time analog domain and discrete time domain. The LACU 328428, if implemented digitally, receives samples of the input signal directly from the input ADC (not shown) and simultaneously from a feedback ADC (not shown) via the FBSS 432. Note that the available signal processing bandwidth, BW, of the ADC and DAC devices is limited by a sample frequency, F_s , and the Nyquist criteria, i.e.

$$BW \leq \frac{F_s}{2}.$$